Load cell Amplifier Unit, **LAU 63.1** (Bipolar voltage output)

FINAL DATA, 2nd. issue.

Property	Specification	Access by	Notes
Load cell Excitation voltage	10 VDC		Short-circuit protected
Load cell Excitation system	4 wire, pins for sense wires available		Balanced or unbalanced
Load cell Drive capability	1 Load cell: 250Ω - 2000Ω		Ohm bridge impedance.
Input range capability and resolution	±66mV input; resolution ≈200nV.		0-±Max Load cell output.
Zero Offset, fixed binary steps	±14mV by 2mV increments	+/- and three DIP-switches	Input at 0 V output.
Option: Zero Offset, fine trim	±1,5mV or 0,07mV/360° turn	20 turn 3/8' pot.	Only if UA73.3 board apply
Relative gain factor, fixed binary steps	Range 1-8*; by 1* increments. (i.e. min. ±8,3mV_{inp} at ±10V_{out})	Three DIP-switches	1*: ±66mV_{inp} at ±10V_{out} 8*: ±8,3mV_{inp}at ±10V_{out}
Option: Relative gain factor, fine trim	1,2* (non-linear)	20 turn 3/8' pot.	Only if UA73.3 board apply
Signal filter, active, low pass	3300; 330; 33 or 3.3Hz	Three DIP-switches	
Voltage Output	±10Vdc; 20mA (i.e. permit 500Ω load)		Short-circuit protected
Linearity	<0.010 (one part in 10 000)		% of Full Scale
Temperature Effect on Zero	Zero<50ppm/°C		Given no optional trim pots
Temperature Effect on Gain	Gain<50ppm/°C		Given no optional trim pots
Temperature Range	Compensated -10°C/+40°C; Operating -20°C/+50°C		
Power Supply	12-24VDC 70mA Regulated supply,	Not isolated	Excess voltage, reversed polarity and ESD protected.
External connections	Load cell: Single row, 10 pos. Power supply and output: Dual row, 10 positions. All 2.54mm pitch.		
Dimensions, weight and mounting	L81.3 * W30.5 * H6,1mm; Weighing 26 gram ; Bolt mounting 2*ø3,2mm holes in one end.		
Environmental protection	IP40 – enclosure of tinned steel, attached to the PCB.		
Conform to Council Directives	CE in accordance with 73/23/EEC; 93/98/EEC and 89/336/EEC		